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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of

Motoi ASHIDA

Serial No.: 10/801,657

Filed: March 17, 2004

For: SEMICONDUCTOR MEMORY DEVICE WITH STATIC MEMORY CELLS



: Customer Number: 20277

: Confirmation Number: 1700

: Group Art Unit: 2811

: Examiner: Unknown

REQUEST FOR CORRECTED FILING RECEIPT

Mail Stop Application
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Attached is a copy of the Filing Receipt received from the U.S. Patent and Trademark Office in the above-referenced application. It is noted that the second Foreign Application No. should read JAPAN 2003-416835(P).. Attached is a copy of the filing receipt, which evidences the corrections in red ink. It is requested that a corrected filing receipt be issued.

Respectfully submitted,

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APPL NO.	FILING OR 371 (c) DATE	ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO	DRAWINGS	TOT CLMS	IND CLMS
10/801,657	03/17/2004	2811	770	67161-146	10	19	1

McDermott, Will & Emery
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Washington, DC 20005-3096

CONFIRMATION NO. 1700

FILING RECEIPT



OC000000012845072

Date Mailed: 06/02/2004

Receipt is acknowledged of this regular Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Filing Receipt Corrections, facsimile number 703-746-9195. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

Applicant(s)

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Assignment For Published Patent Application

RENESAS TECHNOLOGY CORP.;

Domestic Priority data as claimed by applicant

Foreign Applications

JAPAN 2003-130244(P) 05/08/2003

JAPAN 2003-415835(P) 12/15/2003

416835

If Required, Foreign Filing License Granted: 06/02/2004

Projected Publication Date: 11/11/2004

Non-Publication Request: No

Early Publication Request: No

Title

Semiconductor memory device with static memory cells

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MW&E

Preliminary Class

257

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